

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	235	((optimiz\$5 or enhanc\$5 or improv\$4) near2 (loop\$3 or iterat\$3)).clm.	US-PGPUB	OR	ON	2006/07/18 14:56
L2	3	((part\$3 or incomplete or "not complete" or "not total") near2 trac\$3 near3 profil\$3).clm.	US-PGPUB	OR	ON	2006/07/18 14:56
L3	0	((augment\$5 or increas\$2 or greater) near2 trac\$3) with superset with ((part\$3 or incomplete or "not complete" or "not total") near trac\$3) with subset with (loop\$3 or iterat\$3)).clm.	US-PGPUB	OR	ON	2006/07/18 14:58
L4	3	((unroll\$3 or unwind\$3 or unfold\$3) near2 (augment\$5 or increas\$2 or greater)) with (loop\$3 or iterat\$3)).clm.	US-PGPUB	OR	ON	2006/07/18 14:59
L5	321	(augment\$5 or increas\$2 or greater) near path.clm.	US-PGPUB	OR	ON	2006/07/18 14:59
L6	529	((combin\$5 or merg\$3 or add\$5) with (augment\$5 or increas\$3 or greater) with (part\$3 or incomplete or "not complete" or "not total")) .clm.	US-PGPUB	OR	ON	2006/07/18 15:00
L7	1	((candidate near augment\$5 near path) with trac\$3 with probabilit\$3 with (loop\$3 or iterat\$3) with (sum or add5) with (similar or alike or equivalent) with (part\$3 or incomplete or "not complete" or "not total")) .clm.	US-PGPUB	OR	ON	2006/07/18 15:00
L8	0	1 and 2 and 3 and 4 and 5 and 6 and 7	US-PGPUB	OR	ON	2006/07/18 14:51
L9	1	1 and 2 and 4 and 5 and 6 and 7	US-PGPUB	OR	ON	2006/07/18 14:51
L10	11732	((optimiz\$5 or enhanc\$5 or improv\$4) near2 (loop\$3 or iterat\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/18 14:56
L11	74	((part\$3 or incomplete or "not complete" or "not total") near2 trac\$3 near3 profil\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/18 14:57

## EAST Search History

L12	0	((augment\$5 or increas\$2 or greater) near2 trac\$3) with superset with ((part\$3 or incomplete or "not complete" or "not total") near trac\$3) with subset with (loop\$3 or iterat\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/18 14:58
L13	17	((unroll\$3 or unwind\$3 or unfold\$3) near2 (augment\$5 or increas\$2 or greater)) with (loop\$3 or iterat\$3))	US-PGPUB	OR	ON	2006/07/18 14:59
L14	46	((unroll\$3 or unwind\$3 or unfold\$3) near2 (augment\$5 or increas\$2 or greater)) with (loop\$3 or iterat\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/18 14:59
L15	13627	(augment\$5 or increas\$2 or greater) near path	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/18 14:59
L16	34845	((combin\$5 or merg\$3 or add\$5) with (augment\$5 or increas\$3 or greater) with (part\$3 or incomplete or "not complete" or "not total"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/18 15:00
L17	1	((candidate near augment\$5 near path) with trac\$3 with probabilit\$3 with (loop\$3 or iterat\$3) with (sum or add5) with (similar or alike or equivalent) with (part\$3 or incomplete or "not complete" or "not total"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/18 15:01
L18	1	10 and 11 and 14 and 15 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/18 15:05
L19	2062	((717/150-151,158,160,130-131) or (712/233,241)).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/07/18 15:06
L20	1843	19 and (@ad<"20030828" or @rlad<"20030828")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/18 15:07
L21	0	20 and 18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/18 15:07

# Interference Search

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	235	((optimiz\$5 or enhanc\$5 or improv\$4) near2 (loop\$3 or iterat\$3)).clm.	US-PGPUB	OR	ON	2006/07/18 14:28
L2	3	((part\$3 or incomplete or "not complete" or "not total") near2 trac\$3 near3 profil\$3).clm.	US-PGPUB	OR	ON	2006/07/18 14:30
L3	0	((augment\$5 or increas\$2 or greater) near2 trac\$3) with superset with ((part\$3 or incomplete or "not complete" or "not total") near trac\$3) with subset with (loop\$3 or iterat\$3)).clm.	US-PGPUB	OR	ON	2006/07/18 14:32
L4	3	((unroll\$3 or unwind\$3 or unfold\$3) near2 (augment\$5 or increas\$2 or greater)) with (loop\$3 or iterat\$3)).clm.	US-PGPUB	OR	ON	2006/07/18 14:34
L5	321	(augment\$5 or increas\$2 or greater) near path.clm.	US-PGPUB	OR	ON	2006/07/18 14:35
L6	529	((combin\$5 or merg\$3 or add\$5) with (augment\$5 or increas\$3 or greater) with (part\$3 or incomplete or "not complete" or "not total")).clm.	US-PGPUB	OR	ON	2006/07/18 14:48
L7	1	((candidate near augment\$5 near path) with trac\$3 with probabilit\$3 with (loop\$3 or iterat\$3) with (sum or add5) with (similar or alike or equivalent) with (part\$3 or incomplete or "not complete" or "not total")).clm.	US-PGPUB	OR	ON	2006/07/18 14:50
L8	0	1 and 2 and 3 and 4 and 5 and 6 and 7	US-PGPUB	OR	ON	2006/07/18 14:51
L9	1	1 and 2 and 4 and 5 and 6 and 7	US-PGPUB	OR	ON	2006/07/18 14:51



unrolling loop with partial hot trace

1990

- 2003

Search

Adv  
Sch  
Sch
☒ Search only in Engineering, Computer Science, and Mathematics.

☐ Search in all subject areas.

"with" is a very common word and was not included in your search. [\[details\]](#)

**Scholar**

Results 1 - 50 of about 82 for unrolling loop with partial hot trace. (0.17 seconds)

Dynamo: a transparent dynamic optimization system - group of 31 » [All articles](#) [Recent articles](#)

V Bala, E Duesterwald, S Banerjia - Proceedings of the ACM SIGPLAN 2000 conference on ..., 2000 - portal.acm.org

... execution follows the selected **hot trace** most of the ... opportunities typically result from **partial** redundancies in ... **loop** invariant code motion and **loop unrolling**. ...

Cited by 315 - [Web Search](#)

Selecting long atomic traces for high coverage - group of 5 »

R Rosner, M Moffie, Y Sazeides, R Ronen - Proceedings of the 17th annual international conference on ..., 2003 - portal.acm.org

... **Loop unrolling** needs to be considered in combination with a ... The amount of **unrolling** is another important design ... Figure 4-1 **Partial** Ordering of Local and Global ...

Cited by 9 - [Web Search](#)

An architectural framework for runtime optimization - group of 6 »

MC Merten, AR Trick, RD Barnes, EM Nystrom, CN ... - Computers, IEEE Transactions on, 2001 - ieeexplore.ieee.org

... fetch performance such as **loop unrolling**, **partial** function inlining ... per cycle and preserving optimized **hot** spots for ... The **Trace** Generation Unit is discussed more ...

Cited by 36 - [Web Search](#) - [BL Direct](#)

Dynamic hot data stream prefetching for general-purpose programs - group of 12 »

TM Chilimbi, M Hirzel - Proceedings of the ACM SIGPLAN 2002 Conference on ..., 2002 - portal.acm.org

... transfer control to checks at procedure entries or **loop** back-edges. ... headLen = 3 and there is only one **hot** data stream ... when v.seen = 1, we have a **partial** match a ...

Cited by 59 - [Web Search](#) - [BL Direct](#)

Memory bank disambiguation using modulo unrolling for Raw machines - group of 7 »

R Barua, W Lee, S Amarasinghe, A Agarwal - High Performance Computing, 1998. HIPC'98. 5th International ..., 1998 - ieeexplore.ieee.org

... that proces- sor, creates a network **hot-spot**, and ... possi- ble for **loops** with unknown **loop** bounds. ... is to devise a method using **partial unrolling**, whose resulting ...

Cited by 19 - [Web Search](#)

PARROT: Power Awareness through Selective Dynamically Optimized Traces - group of 2 »

R Rosner, Y Almog, M Moffie, N Schwartz, A ... - PACS'03, 2003 - Springer

... achieves the effects of explicit **loop unrolling**, an enabler ... and global **trace** transformations such as **partial** renaming and ... **trace-cache** based optimized **hot** traces ...

Cited by 3 - [Web Search](#)

Dynamic native optimization of interpreters - group of 18 »

GT Sullivan, DL Bruening, I Baron, T Garnett, S ... - Proceedings of the 2003 workshop on Interpreters, Virtual ..., 2003 - portal.acm.org

... and tracked, thus enabling continued **partial** evaluation ... branch \*/ dynamorio logical  
 direct jump ( pc ); } goto loop ; ... if ( lbb- >count > hot threshold ) { set ...  
 Cited by 14 - [Web Search](#)

Instruction scheduling for instruction level parallel processors - group of 3 »  
 P Faraboschi, JA Fisher, C Young - Proceedings of the IEEE, 2001 - [ieeexplore.ieee.org](#)  
 ... this a problem, as the **hot trace** through the ... compiler chose to place enlargement  
 (loop unrolling) before trace ... These constraints induce a **partial** ordering on ...  
 Cited by 11 - [Web Search](#) - [BL Direct](#)

Overview of the IBM Java Just-in-Time Compiler - group of 8 »  
 T Suganuma, T Ogasawara, M Takeuchi, T Yasue, M ... - IBM Systems Journal, 2000 - [research.ibm.com](#)  
 ... always inlined regardless of the **hot spot**. ... **Partial** redundancy elimination 14 is also  
 used to ... **loop** optimizations, such as **loop unrolling**, **loop** interchange, and ...  
 Cited by 122 - [Cached](#) - [Web Search](#) - [BL Direct](#)

[BOOK] Instruction Assignment for Clustered VLIW DSP Compilers: A New Approach. - group of 5 »  
 G Desoli - 1998 - [hwswworld.com](#)  
 ... a global scheduling technique known as **trace** scheduling [5 ... DAG (like those generated  
 by **loop unrolling**) to be ... found that allocating each **partial** component to a ...  
 Cited by 44 - [View as HTML](#) - [Web Search](#) - [Library Search](#) - [BL Direct](#)

Aggressive compiler optimization and parallelization with thread-level speculation - group of 2 »  
 LL Chen, Y Wu - Parallel Processing, 2003. Proceedings. 2003 International ..., 2003 - [ieeexplore.ieee.org](#)  
 ... This example forms a simple **trace** region to demonstrate the ... can be completely unrolled  
 along the **hot path**. **Loop unrolling** can be applied when a **loop** entry is ...  
[Web Search](#) - [BL Direct](#)

Transparent dynamic optimization: The design and implementation of Dynamo - group of 5 »  
 V Bala, E Duesterwald, S Banerjia - Hewlett Packard Laboratories Technical Report HPL-1999-78. ..., 1999 -  
[hwswworld.com](#)  
 ... 41 8.1.1 Exposed **partial** redundancies ... in its normal mode of operation, completing  
 the Dynamo execution **loop**. ... is that if an exit from a **hot trace** itself gets **hot** ...  
 Cited by 61 - [View as HTML](#) - [Web Search](#)

Run-time code generation in C++ as a foundation for domain-specific optimisation - group of 5 »  
 O Beckmann, A Houghton, PHJ Kelly, M Mellor - Proceedings of the 2003 Dagstuhl Workshop on Domain-  
 Specific ..., 2003 - Springer  
 ... Offline **partial** evaluation systems like these rely on binding ... This facilitates complete  
**unrolling** of the inner two ... and the then-remaining innermost **loop** (the for ...  
 Cited by 6 - [Web Search](#) - [BL Direct](#)

[PS] Trace caches in the context of other cache enhancements  
 Z Patel - University of Witwatersrand, Tech. Rep, 2000 - [cs.wits.ac.za](#)  
 ... These **hot spots** are detected by using the history ... Both **partial** matching and inactive  
 issue were found ... unless compiler optimisations like **loop unrolling** are done ...  
 Cited by 2 - [View as HTML](#) - [Web Search](#)

Energy-monitoring tool for low-power embedded programs - group of 8 »  
 D Shin, H Shim, Y Joo, HS Yun, J Kim, N Chang - Design & Test of Computers, IEEE, 2002 - [ieeexplore.ieee.org](#)  
 ... however, the programmer moves energy **hot spots** to ... Reducing **loop** overhead further  
 improved the energy ... Figure 7 shows **partial** energy-monitoring results of the ...  
 Cited by 6 - [Web Search](#) - [BL Direct](#)

Space-time scheduling of instruction-level parallelism on a raw machine - group of 14 »

W Lee, R Barua, M Frank, D Srikrishna, J Babb, V ... - ACM SIGOPS Operating Systems Review, 1998 - portal.acm.org

... The VLIW Multiflow **TRACE** machine is a machine which adopts such a solution [19].

On ... tus to minimize congestion and route data around **hot** spots. ...

Cited by 102 - Web Search - BL Direct

[PS] Trace Cache Design for Wide-Issue Superscalar Processors - group of 2 »

SJ Patel - 1999 - crhc.uiuc.edu

... 106.8.4 A **loop** composed of 3 fetch blocks. . . . . dealing with the **partial** fetch

problem is the **trace** cache and it is described in Chapter 2. ...

Cited by 8 - View as HTML - Web Search - Library Search

A framework for reducing the cost of instrumented code - group of 12 »

M Arnold, BG Ryder - ACM SIGPLAN Notices, 2001 - portal.acm.org

... of instrumentation operations per **loop** iteration ... Combining both variations

(**Partial**-Duplication and No-Duplication ... selectively instrument only the **hot** meth- ...

Cited by 100 - Web Search - BL Direct

A Case for Automatic Run-Time Code Optimization - group of 2 »

E Feigin - Undergraduate thesis, Harvard University, April, 1999 - eecs.harvard.edu

... Some, such as **loop unrolling**, increase code size. ... driven compilation has been used

as a **partial** solution to ... the **hot** code in units conducive to optimization. ...

Cited by 11 - View as HTML - Web Search

Automating Selective Dynamic Compilation - group of 4 »

MU Mock - 2002 - cs.pitt.edu

... executes, they try to identify **hot** spots, ie, regions of ... 1) a sophisticated form

of **partial**-evaluation-style ... can result in complete **loop unrolling** by creating ...

Cited by 5 - View as HTML - Web Search - Library Search

C-based SoC design flow and EDA tools: an ASIC and system vendor perspective - group of 2

»

K Wakabayashi, T Okamoto - Computer-Aided Design of Integrated Circuits and Systems, ..., 2000 -

ieeexplore.ieee.org

... when all array indexes in the **loop** become constants after complete **unrolling**. ... a register

file with an address generator for a **partial** unrolled **loop**. ...

Cited by 36 - Web Search - BL Direct

The POWER4 Processor Introduction and Tuning Guide - group of 46 »

S Behling, R Bell, P Farrell, H Holthoff, FO' ... - IBM Redbooks, November, 2001 - jumpdoc.fz-juelich.de

... These include **trace** and debug facilities used for First Failure Data Capture,

built-in self-test (BIST) facilities, performance monitoring unit (PMU), an ...

Cited by 21 - View as HTML - Web Search

Dynamic Optimization Infrastructure and Algorithms for IA-64

KM Hazelwood - 2000 - lib.ncsu.edu

... A thesis submitted to the Graduate Faculty of North Carolina State University in

**partial** fulfillment of the requirements for the Degree of Master of ... **hot trace**. ...

Cited by 1 - View as HTML - Web Search

METRIC: tracking down inefficiencies in the memory hierarchy via binary rewriting - group of 8

»

J Marathe, F Mueller, T Mohan, BR de Supinski, SA ... - Code Generation and Optimization, 2003. CGO 2003.

..., 2003 - ieeexplore.ieee.org

... In the following, the soft- ware infrastructure for **partial trace** generation is

detailed. ... the scope structure of the target, ie, the function/loop entry and ...  
 Cited by 10 - [Web Search](#)

Time Optimal Software Pipelining of Loops with Control Flows - group of 6 »  
 HS Yun, J Kim, SM Moon - International Journal of Parallel Programming, 2003 - Springer  
 ... effect of fine-grain parallelization with full **loop unrolling**. ... unless iterations  
 of the **hot paths** stay ... Time Optimal Software Pipelining of **Loops** with Control ...  
 Cited by 1 - [Web Search](#) - [BL Direct](#)

Path-Sensitive, Value-Flow Optimizations of Programs - group of 4 »  
 R Bodik - 1999 - cs.pitt.edu  
 ... Submitted to the Graduate Faculty of Arts and Sciences in **partial** fulfillment of  
 the ... The instruction schedule of the **loop**, before and after the removal of load ...  
 Cited by 2 - [View as HTML](#) - [Web Search](#)

PC software performance tuning - group of 3 »  
 M Atkins, R Subramaniam - Computer, 1996 - ieeexplore.ieee.org  
 ... They execute fastest if backward branches are taken, as in a **loop**, and forward ... **Partial**  
 stalls occur when code reads a 32-bit register (EAX, EBX, ECX, or EDX ...  
 Cited by 18 - [Web Search](#) - [BL Direct](#)

Stride prefetching by dynamically inspecting objects - group of 4 »  
 T Inagaki, T Onodera, H Komatsu, T Nakatani - ACM SIGPLAN Notices, 2003 - portal.acm.org  
 ... After this **partial** interpretation, we analyze the **trace** of the memory addresses. ...  
 we can increase the amount of com- putation by **unrolling** the **loop**. ...  
 Cited by 7 - [Web Search](#) - [BL Direct](#)

Specialization Tools and Techniques for Systematic Optimization of System Software - group  
 of 27 »  
 D Mc NAMEE, J WALPOLE, C PU, C COWAN, C KRASIC, A ... - ACM Transactions on Computer Systems,  
 2001 - portal.acm.org  
 ... Page 3. This paper is organized as follows. Section 2 describes the fundamentals  
 of specialization: specialization predicates, **partial** evaluation, and guards. ...  
 Cited by 36 - [Web Search](#) - [BL Direct](#)

ONLINE PROFILING AND FEEDBACK-DIRECTED OPTIMIZATION OF JAVA - group of 7 »  
 M ARNOLD - 2002 - cs.rutgers.edu  
 ... **Loop Unrolling** . . . 3.7. Example of **Partial**-Duplication . ... The guarantee is currently  
 met by simply placing yieldpoints on all method entries and **loop** backedges. ...  
 Cited by 7 - [View as HTML](#) - [Web Search](#) - [Library Search](#)

A dynamic optimization framework for a Java just-in-time compiler - group of 2 »  
 T Suganuma, T Yasue, M Kawahito, H Komatsu, T ... - Proceedings of the 16th ACM SIGPLAN conference on  
 Object ..., 2001 - portal.acm.org  
 ... here is to identify the **hot paths** for ... is decremented depending on the **loop** iteration  
 count. ... for guid- ing branch directions in **partial** redundancy optimizations ...  
 Cited by 58 - [Web Search](#) - [BL Direct](#)

Modulo scheduling with isomorphic control transformations - group of 2 »  
 NJ Warter - 1993 - crhc.uiuc.edu  
 ... 111 5.5.5 II versus **unrolling** : : : : 114 ... b **Loop**  
 data dependence graph. ... a **Partial** CFG. ...  
 Cited by 25 - [View as HTML](#) - [Web Search](#) - [Library Search](#)

[book] Optimization and Parallelization of a Commodity Trade Model for the SP1, Using Parallel  
 Programming ... - group of 2 »

D Bergmark, M Pottle - 1994 - [historical.ncstrl.org](http://historical.ncstrl.org)

... data parallel, **loop** distribution, **loop** fusion, **trace** analyzers ... A **loop** analysis and a **hot spot** analysis ... Scalar optimizations include **loop unrolling**, dead code ...

[View as HTML](#) - [Web Search](#) - [Library Search](#)

### Porting GCC to the AMD64 architecture - group of 17 »

J Hubicka - Proceedings of the GCC Developers Summit, 2003 - [gd4.tuwien.ac.at](http://gd4.tuwien.ac.at)

... GCC has been ported to, some AMD64 features are unique, such as CISC instruction set, gener- ally usable IP relative addressing, **partial sup-** port for 64-bit ...

Cited by 3 - [View as HTML](#) - [Web Search](#)

### Interprocedural Path Profiling and the Interprocedural Express-Lane Transformation - group of 3 »

DG Melski - 2002 - [cs.wisc.edu](http://cs.wisc.edu)

... A DISSERTATION SUBMITTED IN **PARTIAL** FULFILLMENT OF THE REQUIREMENTS FOR THE DEGREE OF ... The express-lane transformation isolates and duplicates **hot** paths in a ...

Cited by 6 - [View as HTML](#) - [Web Search](#) - [Library Search](#)

### Using Performance Bounds to Guide Code Compilation and Processor Design

H Zhou - 2003 - [lib.ncsu.edu](http://lib.ncsu.edu)

... duplication, **loop unrolling**, and if-conversion ... A dissertation submitted to the Graduate Faculty of North Carolina State University in **partial** fulfillment of the ...

Cited by 1 - [View as HTML](#) - [Web Search](#)

### Dynamic Optimization through the use of Automatic Runtime Specialization - group of 2 »

J Whaley - 1999 - [stanford.edu](http://stanford.edu)

... **Loop unrolling** and some register allocation algorithms make a simplifying assumption that a **loop** will execute a ... For example, using **partial** evaluation or other ...

Cited by 18 - [View as HTML](#) - [Web Search](#) - [Library Search](#)

### [PS] Path-based compilation - group of 5 »

RC Young - 1998 - [eecs.harvard.edu](http://eecs.harvard.edu)

... in **partial** fulfillment of the requirements ... but one cannot tell anything about the order in which the **hot spots** ... For example, **loop unrolling** makes extra cop- ...

Cited by 23 - [View as HTML](#) - [Web Search](#) - [Library Search](#)

### [BOOK] Compiler Support for Machine Independent Parallelization of Irregular Problems - group of 4 »

R von Hanxleden - 1992 - [historical.ncstrl.org](http://historical.ncstrl.org)

... Steph and Jerry, happy **hot-tub** hosts ... 3.1 **Partial** Redundancy Elimination : : : : 24 ... 4.4 **Loop** Flattening from the Compiler's ...

Cited by 8 - [View as HTML](#) - [Web Search](#) - [Library Search](#)

### [PS] Compile-time performance prediction of scientific programs - group of 4 »

GCI Casti caval - 2000 - [cs.uiuc.edu](http://cs.uiuc.edu)

... low-level drivers for DOS, to the database gateways, **hot-cup** models ... 67 4.5 A **partial** sum hierarchy . ... to either a **loop** nest, a subroutine, or an entire program. ...

Cited by 12 - [View as HTML](#) - [Web Search](#) - [Library Search](#)

### Dynamic Optimization of Interpreters using DynamoRIO - group of 4 »

I Baron - 2003 - [cag.csail.mit.edu](http://cag.csail.mit.edu)

... such as constant propagation, dead code elimination, and **partial** evaluation to further ... We also present two techniques to avoid **unrolling** native **loops**. ...

Cited by 2 - [View as HTML](#) - [Web Search](#) - [Library Search](#)

### [PS] Latency-directed Multithreaded Computation and Its Architectural Support - group of 2 »



X Fan - 1994 - [tams-www.informatik.uni-hamburg.de](http://tams-www.informatik.uni-hamburg.de)

... Figure 6.10. Pipeline stage 2 - Instruction fetch and **partial** decode ... single-chip processors including high-speed oating-point arithmetic. Some **hot** chips have ...

[View as HTML](#) - [Web Search](#) - [Library Search](#)

### Identifying and Exploiting Spatial Regularity in Data Memory References - group of 16 »

T Mohan, BR de Supinski, SA McKee, F Mueller, A ... - Proceedings of the Proceedings of the ACM/IEEE SC2003 ..., 2003 - [portal.acm.org](http://portal.acm.org)

... Such temporal information can be used for **partial** regeneration of ... accepts directives for aggressive prefetch- ing, inner **loop** fission, and **unrolling** [21 ...

Cited by 6 - [Web Search](#)

### Tolerating latency in multiprocessors through compiler-inserted prefetching - group of 3 »

TC Mowry - ACM Transactions on Computer Systems (TOCS), 1998 - [portal.acm.org](http://portal.acm.org)

... (2) Isolate the predicted dynamic miss instances using **loop**-splitting techniques such as peeling, **unrolling**, and strip-mining. ...

Cited by 53 - [Web Search](#) - [BL Direct](#)

### The role of APL and J in high-performance computation - group of 4 »

R Bernecky - ACM SIGAPL APL Quote Quad, 1993 - [portal.acm.org](http://portal.acm.org)

... Today's **hot** code may be tomorrow's dog – unrolled **loops**, which ... the details of cache management, data distribution, code scheduling, or **loop unrolling**. ...

Cited by 7 - [Web Search](#) - [BL Direct](#)

### Region-based Register Allocation for EPIC Architectures - group of 4 »

H Kim - 2001 - [crest.gatech.edu](http://crest.gatech.edu)

... A dissertation submitted in **partial** fulfillment ... 15 2.2 Code Shuffle for **Trace** Schedule . ... mations like function inlining and **loop unrolling** to exploit ILP well ...

Cited by 5 - [View as HTML](#) - [Web Search](#) - [Library Search](#)

### AN ANALYSIS OF A NOVEL APPROACH TO DYNAMIC OPTIMIZATION - group of 2 »

BM Fahs - 2003 - [crhc.uiuc.edu](http://crhc.uiuc.edu)

... Submitted in **partial** fulfillment of the requirements for the degree ... limited scope of a **trace**. ... For example, memory addresses or **loop** indices are generated this ...

[View as HTML](#) - [Web Search](#) - [Library Search](#)

### Static correlated branch prediction - group of 5 »

C Young, MD Smith - ACM Transactions on Programming Languages and Systems ( ..., 1999 - [portal.acm.org](http://portal.acm.org)

... 1997], and **partial** redundancy elimina- tion [Gupta et al ... branch pattern may not identify a unique **trace**, since two ... where H dominates L is called a **loop** or back ...

Cited by 19 - [Web Search](#) - [BL Direct](#)

### System-level power optimization: techniques and tools - group of 17 »

L Benini, G de Micheli - ACM Transactions on Design Automation of Electronic Systems ..., 2000 - [portal.acm.org](http://portal.acm.org)

... Hence, most algorithms have **loops**, branches, and procedure calls that create a nonuniform distribution for the number of times each operation is executed. ...

Cited by 208 - [Web Search](#) - [BL Direct](#)

### Design space optimization of embedded memory systems via data remapping - group of 6 »

KV Palem, RM Rabbah, VJ Mooney III, P Korkmaz, K ... - Proceedings of the joint conference on Languages, compilers ..., 2002 - [portal.acm.org](http://portal.acm.org)

... Locality Enhancing Algorithm • **Loop** transformations • Data reorganization Software ... access patterns along program **hot-spots** [1 ... Block Size B and **Trace** T R = (k ...

Cited by 16 - [Web Search](#) - [BL Direct](#)

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unrolling loop with partial hot trace

1990

- 2003

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☒ Search only in Engineering, Computer Science, and Mathematics.

☐ Search in all subject areas.

"with" is a very common word and was not included in your search. [\[details\]](#)

Scholar

Results 51 - 82 of 82 for unrolling loop with partial hot trace. (0.13 seconds)

[\[PS\] An Expert Advisor for Parallel Programming Environments and Its Realization within the Framework of ...](#) [All articles](#) [Recent articles](#)

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